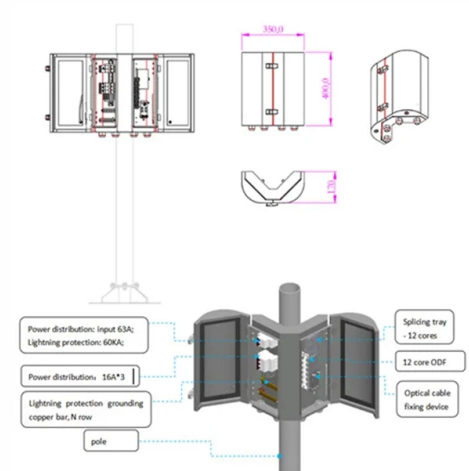


St interface



AI Overview

The term "ST interface" can refer to hardware interfaces from STMicroelectronics, the Avalon-ST interface in embedded systems, or the st0 secure tunnel interface in networking.

1. STMicroelectronics Interfaces

STMicroelectronics offers a wide range of interface ICs and transceivers for connecting devices and systems. These include standard interfaces like RS-232, RS-422, RS-485, LVDS, USB, as well as I/O expanders, level translators, and application-specific interfaces for smart cards and Ethernet. Many transceivers feature auto-power-down, standby functions, and high ESD protection, supporting low-power, high-speed communication across multiple nodes. ST interfaces also provide development tools, evaluation boards, and certified protocol stacks for designers.

2. Avalon-ST Interface

The Avalon-ST interface is used in embedded systems, particularly in Intel/Altera FPGA designs. It is a streaming interface that allows data transfer between cores or peripherals. Key features include:

- Transmitter and receiver logic for streaming data
- Host and agent modes for master/slave communication
- Configuration options for SPI clock rate, select signals, and timing
- Software programming model with hardware access routines and register maps

Avalon-ST is commonly used for high-speed serial peripheral communication and supports multi-agent environments.

3. Secure Tunnel (st0) Interface

In networking, particularly on Juniper virtual routers, the st0 interface is a secure tunnel interface used for route-based VPNs. It routes cleartext traffic into an IPsec VPN tunnel and supports:

- Placement in nondefault routing instances
- Configuration in point-to-point or multipoint modes
- Subunit configuration for different routing instances

This interface ensures VPN traffic functions correctly even with overlapping IP addresses across virtual routers, providing flexibility in secure network design.

Summary

The meaning of "ST interface" depends on context:

- STMicroelectronics hardware: physical and logical interfaces for device communication
- Avalon-ST: streaming interface in e...

Article Content

STSW-IMG005

VL53L0X application programming interface (API) source code (C language). Full ranging features control. API structured in a way it can be easily ported/compiled on any micro-controller platform.

STMicroelectronics eases and accelerates user

With the latest version of the TouchGFX software package, STMicroelectronics has made it easier to build great user interfaces (UIs) for

Overview of ST-LINK derivatives

Introduction In this document, ST-LINK is a generic name that refers to the different implementations of a debugger/programmer probe interface for STMicroelectronics microcontrollers. ST-LINK is also the

Optical fiber connector

They come in various types like SC, LC, ST, and MTP, each designed for specific applications. In all, about 100 different types of fiber optic connectors have been introduced to the market.

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Isolated interfaces

ST isolated digital interfaces transfer high-speed digital signals across a galvanic isolation barrier to help protect sensitive circuitry from high voltages, ground loops and transient surges in industrial

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ST's portfolio of interfaces includes standard interfaces (such as RS-232, RS-422, RS-423, RS-485, LVDS, and USB), I/O expanders, level translators and

STM32H750VB

STM32H750VB Active High-performance and DSP with DP-FPU, Arm Cortex-M7 MCU with 128 Kbytes of Flash memory, 1MB RAM, 480 MHz CPU, L1 cache,

14.7.5. Avalon -ST Interface

Both ST data source and ST data sink interfaces support a ready latency of zero.

How to create a Secure Tunnel (st0) interface in J

Commit the change . The interface is now ready to be bound to an IPsec VPN.

Modification History 2020-06-30: Updated J-Web screen shots.

Guide: Connecting your debugger

There are two commonly used connectors which expose only the SWD (Serial Wire Debug) interface or the full JTAG interface. If you are using one of ST's official Nucleo or Discovery boards, you do not

Secure Tunnel Interface in a Virtual Router

Read this topic to learn about secure tunnel (st0) interface in a virtual router. A secure tunnel interface (st0) is an internal interface that is used by route-based VPNs to route cleartext traffic to an IPsec

STM32 LIN Interface

In this tutorial, we'll explore how to implement LIN communication using STM32 microcontrollers. LIN is single-master, multiple-slave architecture that operates over a single wire, making it ideal for

STM32 Graphic User Interface

Our STM32 graphical user interface ecosystem offers a comprehensive set of tools and resources to help you succeed. Design trends in embedded human-machine

Weather Great St Bernard Pass

Today's and tonight's professional weather forecast for Great St Bernard Pass. Precipitation radar, HD satellite images, and current weather warnings, hourly temperature, chance of rain, and sunshine hours.

Interface IC and Transceiver IC

ST's portfolio of interfaces includes standard interfaces (such as RS-232, RS-422, RS-423, RS-485, LVDS, and USB), I/O expanders, level translators, application-specific interfaces for smart cards and

SC and ST connectors

ST (Straight Tip) connectors are another key player in the fiber optic connector arena, renowned for their reliability and durability. They were one of the first connector types widely

AshwinD24's gists · GitHub

GitHub Gist: star and fork AshwinD24's gists by creating an account on GitHub.

Juniper Secure Tunnel Interface st0 IP

New to juniper and setting up a site-to-site IPSEC tunnel. Looking to use Route Based, and I see I have to setup a Secure Tunnel Interface (st0.x). I have been searching for hours to

STLINK-V3: New source measurement unit and 5

Explore the STLINK-V3 debug probe with a new source measurement unit and enhanced interface capabilities for STM32 development.

4.1.2. Avalon-ST Interface

The Avalon-ST interface provides a standard, flexible, and modular protocol for data transfers from a source interface to a sink interface. The Avalon-ST interface protocol allows you to

ST-LINK/V2 in-circuit debugger/programmer for STM8 and STM32

Introduction The ST-LINK/V2 is an in-circuit debugger/programmer for the STM8 and STM32 microcontrollers. The single wire interface module (SWIM) and the JTAG/serial wire debugging

Contact Us

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